

Notice of Allowability

Application No.

09/827,644

Applicant(s)

LI ET AL.

Examiner

Art Unit

Daniel R. Sellers

2644

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 16 March 2005.
2. ☒ The allowed claim(s) is/are 13-25.
3. ☒ The drawings filed on 06 April 2001 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

Daniel R. Sellers
571-272-7528

Response to Arguments

1. Applicant's arguments, see pages 6 and 7, filed 16 March 2005, with respect to claims 13-25 have been fully considered and are persuasive. The rejection of claims 13-25 has been withdrawn.

2. The obviousness-type double patenting applied to the claims in the first action is acknowledged as improper. The appropriate serial number 08/851,573 is cited as the parent application from which this application originates.

Allowable Subject Matter

3. Claims 13-25 are allowed.

The following is an examiner's statement of reasons for allowance:

4. The independent claim 13 establishes a method of addressing in a central processing unit within an audio reproduction system. Specifically, the limitation of

"... address circuitry operable to form a memory address of the data word by selecting a first portion of the first address from the index register and combining the first portion of the first address with a first portion of an immediate field selected from the instruction, such that the first portion of the immediate field is a most significant address portion with the first portion of the first address as a least significant address portion, wherein the first portion of the immediate field has a first width and the first portion of the first address has a second width."

is recited. This limitation is not taught or suggested in the prior art of record or newly

cited references of McFarland et al. (U.S. Patent No. 3,614,741), Kotok et al. (U.S.

Patent No. 4,388,685), Suzuki (U.S. Patent No. 5,357,620), Gutttag et al. (U.S. Patent

No. 5,509,129), Laczko, Sr. et al. (U.S. Patent No. 5,644,310), Benbassat et al. (U.S.

Patent No. 5,729,556), Zenda (U.S. Patent No. 5,809,245), Okamoto et al. (U.S. Patent

No. 5,826,057), and the various audio/video compression standards (MPEG 1/2 or AC-3).

5. Regarding the newly cited reference, Okamoto et al. teaches a method for managing a virtual address space by combining two addresses. Okamoto et al. teaches that the new address is a combination of a value in a base register with an address, stored in a register, specified by an instruction. Okamoto does not teach or imply that an immediate value is used in combination with the address in a base register for a new effective address. The specific combination of combining a first portion of the immediate field as the most significant bit and the first portion of the first address as the least significant bit, wherein the two portions have different widths, is not taught or suggested.

6. Regarding Rostoker et al., as stated in the previous action, does not teach a processor with the claimed features and provides no suggestion to do so.

7. The dependent claims 13-20 are allowable due to their dependency on the allowable claim 13.

8. The independent claim 21 is allowable for the same reasons as claim 13.

9. The dependent claims 22-25 are allowable due to their dependency on the allowable claim 21.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel R. Sellers whose telephone number is 571-272-7528. The examiner can normally be reached on Monday to Friday, 9am to 5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Vivian Chin can be reached on 571-272-7848. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DRS


VIVIAN CHIN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600